

Product Data Sheet: DEHNrail modular

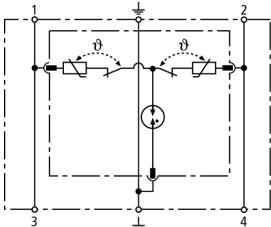


DR M 2P 75 (953 203)

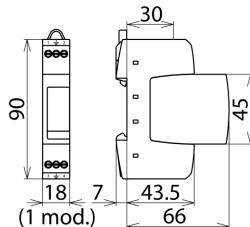
- Two-pole surge arrester consisting of a base element and plug-in protection module
- High discharge capacity due to heavy-duty zinc oxide varistor/spark gap combination
- Energy coordination with other arresters of the Red/Line product family



Figure without obligation



Basic circuit diagram DR M 2P 75



Dimension drawing DR M 2P 75

Two-pole surge arrester consisting of a base part and plug-in protection module

Type	DR M 2P 75
Part No.	953 203
SPD according to EN 61643-11	Type 3
SPD according to IEC 61643-1/-11	Class III
Nominal a.c. voltage (U_N)	60 V
Max. continuous operating a.c. voltage (U_c)	75 V
Max. continuous operating d.c. voltage (U_c)	75 V
Nominal load current a.c. (I_L)	25 A
Nominal discharge current (8/20 μ s) (I_n)	2 kA
Total discharge current (8/20 μ s) [L+N+PE] (I_{total})	4 kA
Combined impulse (U_{oc})	4 kV
Combined impulse [L+N+PE] ($U_{oc total}$)	8 kV
Voltage protection level [L-N] (U_p)	≤ 400 V
Voltage protection level [L/N-PE] (U_p)	≤ 730 V
Response time [L-N] (t_A)	≤ 25 ns
Response time [L/N-PE] (t_A)	≤ 100 ns
Max. mains-side overcurrent protection	25 A gL/gG or B 25 A
Short-circuit withstand capability for mains-side overcurrent protection with 25 A gL/gG	6 kA _{rms}
Operating temperature range (T_u)	-40°C...+80°C
Operating state/fault indication	green / red
Number of ports	1
Cross-sectional area (min.)	0.5 mm ² solid/flexible
Cross-sectional area (max.)	4 mm ² solid/2.5 mm ² flexible
For mounting on	35 mm DIN rails acc. to EN 60715
Enclosure material	thermoplastic, red, UL 94 V-0
Place of installation	indoor installation
Degree of protection	IP 20
Capacity	1 module(s), DIN 43880
Approvals	KEMA, VDE, UL, VdS, CSA
Weight	79 g
Customs tariff number	85363010
GTIN	4013364109698
PU	1 pc(s)

We reserve the right to introduce changes in performance, configuration and technology, dimensions, weights and materials in the course of technical progress. The figures are shown without obligation.